

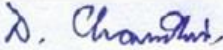
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Bachelor of Technology (Computer Science and Engineering)
Choice Based Credit System (CBCS)

SEMESTER - I

S. No.	COURSE CODE	COURSE NAME	Examination Scheme					Teaching Scheme/Week			CREDITS	TOTAL MARKS
			THEORY			PRACTICAL		Th	T	P		
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)					
1	BTMA101	Applied Mathematics-I	60	20	20	0	0	3	1	0	4	100
2	BTPH101	Applied Physics	60	20	20	30	20	3	1	2	5	150
3	BTCS102	Introduction to Computer Science and Engineering	60	20	20	0	50	3	0	2	4	150
4	BTEC104	Digital Logic & Circuit Design	60	20	20	30	20	3	1	2	5	150
5	BTCE103	Applied Mechanics	60	20	20	30	20	3	1	2	5	150
6	BTCS101	Computer Programming-I	0	0	0	30	20	0	0	2	1	50
7	BTHU101	Communication Skills-I	0	0	0	0	50	0	0	4	2	50
			300	100	100	120	180	15	4	14	26	800

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.


Chairperson
Board of Studies
(Computer Science & Engineering,
Information Technology & Computer Applications)
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore


Deputy Registrar
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore